

C.V

Name : Dr. Mazin Rejab Khalil
Scientific position: Asst. Professor
Specialty: Electronic & Communication Engineering
Nationality: Iraqi
Marital Status: Married
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Certificates:

B.sc /Electronic & Communication Engineering/College of Engineering/University of Mosul/Iraq/1978.
Average Graduation Mark:86,667 %, rank:2nd out of 44).

2. M.sc /Electronic & Communication Engineering/College of Engineering//University of Mosul/Iraq/1985
(accredited by Bradford University, UK).
Thesis Title: Electrical Circuits Analysis and Synthesis Using Hp-Desk Computer.

Jobs and Scientific Grades:

- Asst. lecturer/Technical institute of Mosul/1979-1988.**
- Lecturer/Technical institute of Mosul/1988-1991.**
- Asst .Professor/Technical institute of Mosul/1991-2000.**
- Asst. Professor/ Technical college of Mosul/2001-2014.**
- Dean of technical institute of Dour/1991-1993.**
- Dean of technical institute of Mosul/1993-2001.**
- Dean of Technical College of Mosul/2001-2003.**

Teaching Experiences:

- Digital Signal and Image Processing / M.sc Course.**
- Numerical Analysis/ M.sc course.**

- Microprocessor Based System Design/M.sc course.
- Electronic Measurements/ M.sc & B.sc course.
- Engineering Analysis/ for B.sc students.
- Digital Circuits & Microprocessors/ for B.sc students.
- Integrated Circuits/for B.sc students.
- Medical Instrumentations/ for B.sc students.
- Selected Topics(Image Processing, Neural Networks, Computer Controlled Systems, Microcontrollers, FPGAs) /for B.sc students.
- TV Engineering /for B.sc students.
- Power Electronics/ for B.sc students.

Published Books

- 1.Electronic Measurement Instrumentations/publisher: Dar Al Kuttb/university of Mosul/Editions:1991, 1999.
2. Applied Control Principles/Foundation of Technical Institutes/1993.

Published Papers

Published Papers	
1.	."Prediction Program As An Aid To Design Passive Networks." Engineering And Technology Magazine, Vol.5, No.2, 1987.
2.	"Novel Scheme Microprocessor Based Thyristor System Controller." In Proc. 1st Scientific Conf., FTI, 1988.
3.	"Computer Aided Design of Active Phase Shifting Networks." In Proc.1 st Scientific Conference. , FTI, 1988.
4.	Computer Aided D.C Voltage Calculations of a Transistor Diode Circuit Using Non Linear Analysis." In Proc. 1st Scientific Conf., FTI, 1988.
5.	"Computer Aided Solution of Stability Problem In a Feedback Amplifier." Magazine Of Technical Researches, No.4, 1989.
6.	"Computer Aided Sinusoidal Oscillator Frequency Determination." Magazine Of Technical Researches, No.5, 1989.
7.	"Programmable Auto ranging Micro processor Based Frequency Measuring System." Magazine Of Technical Researches, No.7, 1990.
8.	"Computer Aided Synthesis of L.C Networks." In Proc. 2nd Scientific Conference. , FTI, 1990.
9.	Computer Aided Determination Of Voltages, Currents And Immittance At Any Point in a Transmission Line." Magazine of Technical Researches, 1993.
10.	"Design Of Peaked Broad Banded Monolithic Amplifier Depending On Inductive Characteristics Of Bipolar Junction Transistor Emitter Input Impedance." Magazine Of Technical Researches, No.77, 2001.

11.	“Scheme To Minimize the Effect of Cross talk In Loss less Coupled Transmission Line.” Magazine Of Technical Researches, No.78, 2001.
12.	”Design And Study Of Performance Of Semiconductor Laser As Pressure Sensor.” 4th Scientific Conference, University Of Tikrit, College Of Education, 2002.
13.	.”The Influence of Static Pressure on Transmission Line Loss in Index Fiber”. Magazine of Education and Science, Vol. No. 18.2006.
14.	“Personal Identification With Iris Pattern”, Al-Rafiden Journal of Computer Sciences and Mathematics, Vol.6, No.1, 2009.
15.	“Recognition of Certain Arabic Syllables Based on Wavelet Packet Decomposition”, Research Journal of Aleppo University, Vol.75, 2009.
16.	“Design of A Wavelet Packet Decomposition System Using Field Programmable Gate Arrays (FPGAs)”, ”, Research Journal of Aleppo University, Vol.75, 2009.
17.	“Design of A Field Programmable Gate Arrays (FPGAs) Unit to Compute the Variance as Identifying Feature in Recognition Systems” ”, Research Journal of Aleppo University, Vol.83, 2010.
18.	“ Design of A Hardware Two-layer Perceptron Neural Network Using Field Programmable Gate Arrays (FPGAs) For Speech Recognition” , Al-TAQANI Refereed Scientific Journal,Vol.24 No. 8, 2011, pp91-104.
19.	“ Design and Implementation of A Two-Layer Perceptron Neural Network on A Soft Core Based FPGA”, Gulf University Journal, Sept.2011.
20.	“Development of A Scheme To Connect PC Monitor To Soft-Core Processor”, The Mediterranean Journal Of Computers and Networks, Vol.7, No.1, 2011.
21.	“Position Control Of DC Servomotors Using Soft-Core processor On FPGA To Move Robot Arm ”, Journal of Theoretical and Applied Information Technology, Vol. 32 No. 1,October,2011.
22.	“Using Multiboot Technique to Create a Multiple Embedded Designed Systems”, Journal of Theoretical and Applied Information Technology, Vol. 34 No. 1,December,2011.
23.	“Design of Embedded Image Processing System Using FPGAs”, Journal of Al_Anbar University For Pure Science, Vol.6 No.2,pp.45-49, April,2012.
24	"Proportional-Integral-Derivative Controller Using Embedded System Design Techniques" Journal of tikrit university for engineering science.
25.	"Designing a Parallel Bidirectional Port for a Soft Core Processor System." Tikrit Journal for Engineering Science, Vol.20, No.2,pp29-36,March 2013.
26.	“Designing of a Pulse Width Modulation System Using Embedded System

	Design Techniques", Journal of Theoretical and Applied Information Technology, Vol. 49 No. 1, pp.101-106, March, 2013.
27.	"Designing of a Soft Core Based Wavelet Packet Transform System" Al-TAQANI Refereed Scientific Journal, Vol.26 No. 2, 2013, pp94-106
28.	"Design of a Low Pass FIR Digital Filter Using Field Programmable Gate Arrays (FPGAs)". AL-TAQANI Journal (acceptance paper, No:519, 14/5/2013).
29.	"Synthesis of R-L-C Circuit Using Embedded System Design Techniques FPGA", International Journal for Researches and development in Engineering, Vol.2, Issue.1, June-July 2013, pp13-21.
30.	"Synthesis of RL, LC and RC Circuits Using Embedded Design Techniques", Journal of Babylon University, Acceptance letter No.1659, 3/11/2013, to be published Vol.23, No.2, 2015
31.	"Design Of an Interruptible Soft Core Processor System to Compute Discrete Cosine Transform of Audio Signals", International Conference On Electrical, Communication, Computer, Power And Control Engineering (ICECCPCE13), Iraq, 17-18Dec, 2013.
32.	"Designing FPGA Based VGA System to Display The Components of Synthesized Electrical Circuits Using Embedded Design Techniques", International Conference On Electrical, Communication, Computer, Power And Control Engineering (ICECCPCE13), Iraq, 17-18Dec, 2013.
33.	" Design and Implementation of FFT/IFFT System Using Embedded Design Techniques", International Journal of Engineering and Innovative Technology (IJEIT), Vol3, Issue5, Nov, 2013.
٣٤	Designing of Soft Core Processor System with Direct Memory Access (DMA) Mode, Thi_Qar University Journal for Engineering Sciences, Vol.6, No. 2, 2015
٣٥	Designing Embedded AXI Based Direct Memory Access System, International Journal of Enhanced Research in Science, Technology & Engineering, ISSN: 2319-7463, Vol. 4 Issue 8, August-2015

High Studies Supervision:

a. The following thesis are supervised:

1. Dual Mode Dispersion Free Optical Fibers Profile Synthesis Used As Remote Sensor Operating At 1.55 Micrometer Wavelength, a thesis submitted to the college council of college of science, university of Mosul as partial fulfillment of the requirements for the degree of master of science in remote sensing, 2002.
2. Fiber Optic Sensor for Monitoring Temperature, Pressure and Vacuum Gauge in Autoclave, , a thesis submitted to the college council of college of

education, university of Mosul as partial fulfillment of the requirements for the degree of master of science in physics,2002.

- b. He supervised the following M.sc thesis forwarded to the faculty of Technical College of Mosul as partial fulfillment of the requirements for the degree of master of Technology in Computer Engineering Techniques :

3. Personal Identification Based On Iris Patterns/2005.
4. Smart Cart Based BAM Data Security System/2008.
5. Modeling and Analysis of Synchronous Machine with Damper Winding/2008.
6. Speech Identification Based on Neural Network/2008.
7. Microprocessor Based Design and Implementation of Two Dimensional Digital Filters/2009.
8. Microcontroller Based Design of Adaptive Digital Filter/2009.
9. Design and Implementation of Digital Filter Bank Using FPGA/2010.
10. Design and Implementation of Multilayer N.N for Speech Recognition Using FPGA/2010.
11. Design and Implementation of FPGA Based Driver for a Moving Chair/2011.
12. Design of Soft Core Processor with DDR Synchronous Dynamic RAM/2011.
13. FPGA Based Robot Controller/2012.
14. Design and Implementation of FPGA Soft Core Processor System Interfaced with Flash PROM/2012.
15. Design of a Soft Core Based PID Controller/2013.
16. Introducing an Interface Circuit to Soft Core Processor System to Acquire a Flow of Data/2014.
17. Design of Circuit Synthesis System Configurable on FPGAs Using Embedded Design Techniques (at evaluation stage).
18. Design of AXI Based Direct Memory Access Of Embedded System, (at writing stage).

- c. Member of examination committees for 30 M.sc thesis.

- d. participated in reviewing large number of scientific researches and thesis.

Scientific Conferences, Symposiums and Expeditions

1. 1st scientific conference of foundation of technical institutes(FTI), Baghdad, 1988.
2. 2nd scientific conference of foundation of technical institutes(FTI), Baghdad, 1990.
3. International Visitor Program organized by United States Information Agency, USA, Feb 1990.
4. 6th scientific conference of foundation of technical institutes(FTI), Baghdad, chairman, 1996.
5. 1st scientific conference for Electrical and Electronic Engineering, university of Mosul, 2000.

6. 1st scientific conference of Aleppo university, 2008.
7. 1st scientific conference for information technology (SCIT), university of Mosul, 2008.
8. 2nd conference on computer and information technology (CCIT2012), university of Anbar, 2012.
9. 3rd conference for engineering/college of engineering/university of Tikrit, 2012.
10. Chair of Electronics Sector for International Conference On Electrical, Communication, Computer, Power And Control Engineering (ICECCPCE13)", Iraq, Dec 2013.
11. Technical Program committee (TPC) member of IEEE International Conference On Informatics, Communication And Open Systems(ICICOS 14), Iraq, Feb2, 2014.
12. Technical Program committee (TPC) member of IEEE International Conference" (ICACCI2014)".
13. Technical Program committee (TPC) member of IEEE International Conference(ICCVIAI2014)".
14. Technical Program committee (TPC) member of IEEE International Conference" ICEPIT 2014".
15. Technical Program committee (TPC) member of IEEE International Conference "Vision Net2914".
16. Technical Program committee (TPC) member of IEEE International Symposium"DPNoC2014".
17. IEEE member/Iraq section.

Training Programs and Workshops

Participated in the following training programs and workshops

	Program	Organization & place	Date	Period/ Weeks
1.	Maintenance of Electrical Machines	University of Technology/ Baghdad	1979	2
2.	Teaching Course 1	Faculty Development Center/Baghdad	1980	2
3.	Teaching Course 2	Faculty Development Center/Baghdad	1981	2
4.	Manufacturing Measurement Instruments	Tecquipment Company/England	1981	2
5.	Development program of high level administrative faculties.	Ministry of planning /Baghdad	1993	6
6.	Operating and Servicing of Control Systems and Apparatus.	High Institute of Control And Computers/ Damascus/ Syria	1996	2
7.	Internet Facilities and Applications	Balqua University/ Jordan	1999	2

8.	Workshop on Syllabus Construction Technology	Balqua University/ Jordan	2000	1
9.	Computers and Their Applications	Foundation of Technical Education (FTE)/Baghdad	2000	2
10 .	Workshop on FPGAs and VHDL	Technical College of Mosul/Iraq	2013	2 days

Practical Experience

1. Programming field programmable gate arrays(FPGAs) slices, type Spartan 3E& spartan6, using VHDL language and Embedded Design Techniques.
2. Worked on the following soft ware : Mat lab(6,7,10,11), Integrated Soft ware Environment (ISE 9.2, ISE10.1, ISE13.2), Spice, word, excel and SPAA.
3. Worked on BASIC, C, C++, VISUAL BASIC VHDL programming languages.
4. Participated in a group that designed and manufactured an apparatus to count medical pills during canning process.
5. Participated in a group that designed and manufactured an apparatus to check fuel pumps operation.
6. Directed projects to establish computer networks.
7. Participated in a group for designing, manufacturing and servicing different electronic circuits.
8. Prepared a study to establish a limited industrial project to manufacture telephones.

Languages

a. Arabic

2. English with following certificates:

1. ELTS test from English cultural center, Baghdad, 1990.
2. English language efficiency test, university of Mosul, 2001.
3. English language efficiency test, university of Aleppo/Syria, 2008.