

CURRICULUM VITAE

Bashar Seddik Mohamad-Ali

March 2019



Email: basharsmali@ntu.edu.iq

Research Gate Account link: [https://www.researchgate.net/profile/ Bashar_Mohamad-ali /](https://www.researchgate.net/profile/Bashar_Mohamad-ali/)

EDAS: (777273 – bashar_kalifa@yahoo.com)

EDUCATION

Awarded Degree	Subject of Degree	University or Institution	Date of award
B.Sc.	Electrical and Electronic Engineering	University College of Swansea	1982
M.Sc.	Electronics and communication	University of Mosul	1993

ACADEMIC EMPLOYMENT

Employer	Position	Dates of Employment
Technical Institute of Mosul	Assistant Lecturer	1987-1991
Technical College of Mosul	Assistant Dean	1993-2002
Technical College of Mosul	Head of Department	2002-2004 2007-2009 2011-2013
Technical College of Mosul	Lecturer	1993-2010

Technical College of Mosul	Assistant professor	2010-2018
Ibb University	Visiter Lecturer	2005

HONORS AND AWARDS _____

PROFESSIONAL AFFILIATIONS AND SERVICES

Institute	Affiliations	Dates
Technical Institute of Mosul	Assistant Lecturer	1987-1993
Technical College of Mosul	Lecturer	1993-2010
Technical College of Mosul	Assistant professor	2010-2018
Ibb University, Yemen	Visiter Lecturer	2005

PUBLICATIONS

Title	Type	Date	Publisher
Microprocessor implementation for load commutated cyclo-converter Firing Scheme	Paper	1991	AMSE periodicals, Vol. 35, No. 3, P4955, 1991.
Design of input\ output unit for alwarkaa microcomputer	Paper	1992	Proceeding of the 3rd Scientific Conference for Foundation of Technical Institutes (14th –16th Apr. 1992).
A microcomputer-based Capacitance Meter	Paper	1994	Dirassat Journal, Jordan University, Vol. 21B, No. 2, Mar. 1994, P259-277.
A microcomputer-based Digital IC Tester	Paper	1995	Mutah Journal, Mutah University, Vol.10, No.3, 1995, P61-80.
A microcomputer-based EPROM programmer	Paper	1998	Proceeding of the 6th Scientific Conference for Foundation of Technical Institutes, (23-24th Mar. 1998).
A microcomputer-based In-circuit Digital IC Tester	Paper	1998	Rafidain Engineering Journal, University of Mosul, Vol. 6, No. 2,1998, p 45-59.

A PC-Based Algorithm for Testing a Configured PLD Using Boundary Scan Test	Paper	2008	Dirassat Journal, Jordan University, Vol. 35, No. 2, Oct. 2008.
Design and Implementation of CPLD-based Multilevel Inverter	Paper	2009	Tikrit Journal of Eng. Sciences, University of Tikrit, Vol. 16, No. 4, December 2009.
A test procedure for Boundary scan circuitry in PLDs and FPGAs	Paper	2010	International Arab Journal of Information Technology (IAJIT) University of alzarka, Jordan, Vol. 7, No. 2, April 2010
Implementing digital filter using programmable logic device for education aids	Paper	2010	Al-Taqani Journal, Foundation of Technical Education, Vol. 24, No.2, 2011, p133-144
Low cost Medical Data Acquisition System using AVR-MCU and PC	Paper	2012	Journal of Theoretical and Applied Information Technology, Vol. 42, No 2, Aug. 2012, p217-220. (scopus)
Single Input Adaptive 50 Hz Noise Canceller from ECG Signal	Paper	2013	International conference on Electrical, Communication, Computer, and Power, (ICECCP), 11-12 Dec. 2013, Mosul, Iraq. (scopus)
Adaptive Multitone Noise Cancellation From Speech Signals	Paper	2018	International Journal of Applied Engineering Research, ISSN 0973-4562 Vol. 13, No. 19, 2018, pp. 14102-14107.
Microcomputer Techniques	Book	1991	University of Mosul Press

MANUSCRIPTS IN PREPARATION/SUBMITTED FOR REVIEW

Title	Type	Date
Adaptive Multitone Noise Cancellation From Speech Signals	Paper	2018
A novel Fast Clocking PN Code Acquisition Synchronizer	Paper	2018

CONFERENCE PRESENTATIONS

Title	Conference
A novel Fast Clocking PN Code Acquisition Synchronizer	ICECCPCE 2019, 13-14 Feb., Mosul, Iraq.

TALKS

POSTERS

TEACHING EXPERIENCE

Microcomputer and Microprocessor Architecture,
Microcomputer based systems,
Electrical and Electronic Measurement Devices,
Electronic Circuits.

TEACHING INTERESTS

Microcomputer and Microprocessor Architecture,
Microcomputer based systems,